

8.5. Testarea HIL

8.5.1. Caracteristicile testarii HIL

In “**software-in-the-loop**” testing (SiL) the real software including all resource restrictions (for example, 16-bit or 8-bit integer processing) is tested in a simulated environment or with experimental hardware - this corresponds to the prototyping stage

In “**hardware-in-the-loop**” testing (HiL) the real hardware is used and tested in a simulated environment – this is also part of the prototyping stage

In “**system test**” (ST)
the real system is tested in
its real environment –
this corresponds to the
pre-production stage

